

SET - 1

# II B. Tech I Semester Regular Examinations, Dec - 2015 BASIC ELECTRONICS AND DEVICES

(Electrical and Electronics Engineering)

Time: 3 hours

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Max. Marks: 70

Note: 1.	Question	Paper consists	of two part	ts (Part-A	and Part-B)
2	Answer	ALL the questi	on in Part-	Λ	

2. Answer ALL the question in Part-A

3. Answer any **THREE** Questions from **Part-B** 

# PART -A

1.	a)	What is Hall effect ?Explain.	(3M)
	b)	What are the differences between Drift and Diffusion Capacitance	(3M)
	c)	What is the difference between series and Shunt regulators.	(4M)
	d)	What is meant by Thermal runaway?	(4M)
	e)	Explain the working principle of n-MOSFET	(4M)
	f)	What are the advantages of negative feed back system	(4M)

# <u>PART –B</u>

2.	a)	Explain the classification of Insulators, Semi conductors, and Metals using Energy Band Diagrams	(8M)
	b)	Explain about Mobility, Conductivity and Drift current with suitable diagrams.	(8M)
3.	a)	Draw and explain the construction of PN junction diode	(8M)
	b)	Explain the temperature dependent characteristics of PN junction diode	(8M)
4.	a)	Explain the significance of Rectifiers with filters, and what are the advantages of capacitor filter over Inductor filter	(8M)
	b)	What are the different types of regulators? Explain any one of the regulators.	(8M)
5.	a)	What is DC load line? What is the stability criterion.	(8M)
	b)	Explain the criteria of fixing Operating point.	(8M)
6.	a)	List out the comparisons of power devices.	(8M)
	b)	Draw the small signal model of JFET	(8M)
7.	a)	Explain the principle and operation of Negative feedback amplifier? What are the advantages of Negative feedback amplifier?	(8M)
	b)	What are the four configurations of Negative feedback amplifiers? Explain any one of the configurations.	(8M)



**SET - 2** 

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(Electrical and Electronics Engineering)

Time: 3 hours

Max. Marks: 70

Note: 1. Question Paper consists of two parts (Part-A and Part-B)

2. Answer ALL the question in Part-A

3. Answer any **THREE** Questions from **Part-B** 

# PART –A

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| 1. | a) | What are P and N type semiconductor                              | (3M) |
|----|----|------------------------------------------------------------------|------|
|    | b) | What are the differences between Static and Dynamic resistances? | (4M) |
|    | c) | Define the terms Ripple factor, Regulation and efficiency.       | (4M) |
|    | d) | How does the transistor amplifies the input signal? Explain.     | (3M) |
|    | e) | Sketch the simple FET and Explain.                               | (4M) |
|    | f) | What are the conditions for Oscillation?                         | (4M) |

#### PART -B

| 2. | a)       | Explain Fermi level in Intrinsic, Extrinsic semi conductors with necessary mathematics                                                                                                                                                                                                         | (8M)         |
|----|----------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------|
|    | b)       | With a neat sketch explain the phenomena of Hall Effect in semiconductors                                                                                                                                                                                                                      | (8M)         |
| 3. | a)<br>b) | Explain the operation of varactor diode with neat diagram<br>Design a Zener diode regulator to meet following specifications. Unregulated<br>DC input voltage V $_i$ =15±10V regulated DC output V <sub>o</sub> =8V. I <sub>Zmin</sub> =6mA,<br>I <sub>Zmax</sub> =85mA, Load current Ic=20 Ma | (8M)<br>(8M) |
| 4. | a)       | Define and derive the terms as referred to HWR circuit. i) PIV ii) Average d.c. voltage . iii) RMS current. iv) Ripple factor.                                                                                                                                                                 | (12M)        |
|    | b)       | Explain about over load voltage protection.                                                                                                                                                                                                                                                    | (4M)         |
| 5. | a)<br>b) | Explain the differences of CB, CE and CC configurations.<br>For a transistor the leakage current is 0.2 $\mu$ A in CB configuration, while it is<br>18 $\mu$ A when it is connected in CE configuration. Calculate $\alpha_{DC}$ , and $\beta_{DC}$ of the<br>same transistor                  | (8M)<br>(8M) |
| 6. | a)<br>b) | Draw and explain the low frequency modulo of JFET.<br>For an N channel JFET $I_{DSS}$ is 8mAand $V_P$ is -5V, determine minimum value of $V_{DS}$ for Pinch off region and determine current $I_D$ for $V_{GS}$ of -2V.                                                                        | (8M)<br>(8M) |
| 7. | a)       | Draw the circuit diagram of RC phase shift oscillator, Explain how Barkhausen conditions are satisfied.                                                                                                                                                                                        | (8M)         |
|    | b)       | Explain the principle and operation of Negative feedback amplifier? What are the advantages of Negative feedback amplifier.                                                                                                                                                                    | (8M)         |
|    |          | 1 of 1                                                                                                                                                                                                                                                                                         |              |

#### Code No: RT21023



**SET - 3** 

#### II B. Tech I Semester Regular Examinations, Dec - 2015 BASIC ELECTRONICS AND DEVICES (Electrical and Electronics Engineering)

Time: 3 hours

Max. Marks: 70

Note: 1. Question Paper consists of two parts (Part-A and Part-B)
2. Answer ALL the question in Part-A
3. Answer any THREE Questions from Part-B

#### PART -A

| 1. | a) | What are the differences between the intrinsic and extrinsic semiconductors? | (4M) |
|----|----|------------------------------------------------------------------------------|------|
|    | b) | Explain the dependency of Reverse saturation current on temperature.         | (3M) |
|    | c) | What is the difference between full wave and Bridge rectifiers?              | (4M) |
|    | d) | Define $\alpha$ and $\beta$ of a Transistor.                                 | (3M) |
|    | e) | What are the differences between BJT and FET?                                | (4M) |
|    |    |                                                                              |      |

f) What are the differences between positive and negative feedback systems. (4M)

#### <u>PART –B</u>

- 2. a) State and prove continuity equation. (8M)
  b) In a Ge PN Junction at 300<sup>0</sup> k have the following parameters L<sub>D</sub>=5x10<sup>18</sup>/cm<sup>3</sup>, (8M) L<sub>A</sub>=6x10<sup>16</sup>/cm<sup>3</sup>, n<sub>i</sub>=1.5x10<sup>10</sup>/cm<sup>3</sup>. Calculate the minority electron density in p-region and minority hole density in n-region.
- 3. a) Explain the Zener and Avalanche thermal breakdown mechanisms. What will be (8M) their thermal coefficients?
  - b) Explain the principle and operation of tunnel diode with energy band diagrams (8M)
- a) Construct Bridge rectifier circuit and derive equation for I<sub>DC</sub>, V<sub>DC</sub>, I<sub>RMS</sub>, V<sub>RMS</sub>, and (8M) Rectifier efficiency.
  - b) A diode whose internal resistance is 200hms is to supply power to a load of 1 KΩ (8M) from 110 V (rms) source of supply. Calculate i) Peak load current ii) DC load current iii) AC load current iv) DC diode voltage v) Total input power vi) Peak Inverse voltage vii) % of regulation viii) Efficiency

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# **R13**

- 5. a) Write the current components of NPN transistor. With neat sketch. (8M)
  - b) For a CE amplifier circuit shown in the figure the transistor parameters are (8M)  $R_C = 10K\Omega$ ,  $R_E = 2K\Omega$ , hie =  $2K\Omega$ , hfe = 60, hoe =  $10\mu$ mhos hre  $=1X10^{-4}$ . Assuming  $R_E$  is adequately bypassed by a capacitor CE. Construct small signal equivalent model and Calculate  $A_I$ ,  $A_V$ .



- 6. a) Explain the operation of enhancement mode NMOSFET (8M)
  - b) Explain the Differentiate between BJT, JFET and MOSFET (8M)
- a) Draw the circuit diagram of Colpits oscillator, Explain how Barkhausen conditions (8M) are satisfied.
  - b) Draw crystal controlled oscillator circuit.and Mention some of its applications. (8M)



SET - 4

#### II B. Tech I Semester Regular Examinations, Dec - 2015 BASIC ELECTRONICS AND DEVICES (Electrical and Electronics Engineering)

Time: 3 hours

Max. Marks: 70

#### Note: 1. Question Paper consists of two parts (**Part-A** and **Part-B**) 2. Answer **ALL** the question in **Part-A**

3. Answer any **THREE** Questions from **Part-B** 

# PART -A

| 1 | a) | Explain about Generation and Recombination of Charges                   | (3M) |
|---|----|-------------------------------------------------------------------------|------|
|   | b) | Explain the differences between Avalanche and Zener break down.         | (4M) |
|   | c) | With respect to rectifiers, define $V_{rms}$ , $I_{rms}$ , PIV and TUF  | (4M) |
|   | d) | Draw and explain the self biasing technique.                            | (3M) |
|   | e) | What are the applications of FET.                                       | (4M) |
|   | f) | Write the conditions for sustained Oscillations for Collpits Oscillator | (4M) |

## PART -B

| 2 | a)<br>b) | Explain about Hall Effect. And list the applications.<br>Find the concentration of holes & electrons in the P-type silicon at $300^{0}$ K assuming its resistivity as $0.02\Omega$ -cm, $\mu_{p}$ =475cm <sup>2</sup> /vs, $\eta_{i}$ =1.45×10 <sup>10</sup> /cm <sup>3</sup> . | (8M)<br>(8M) |
|---|----------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------|
| 3 | a)<br>b) | Explain the operation of PIN diode with neat diagram Find the value of DC resistance and AC resistance of a Ge diode at $25^{\circ}$ C with I <sub>0</sub> of 25 $\mu$ A at an applied voltage of 0.2 V.                                                                        | (8M)<br>(8M) |
| 4 | a)       | Define and derive the terms as referred to FWR circuit. i) PIV                                                                                                                                                                                                                  | (8M)         |
|   | b)       | What is the regulated power supply? Explain the operation of Zener diode regulator                                                                                                                                                                                              | (8M)         |
| 5 | a)       | Define h-Parameters of a transistor. Draw and explain the equivalent circuit of BIT                                                                                                                                                                                             | (8M)         |
|   | b)       | Explain the operation of Transistor CE configuration. And also draw the input and output characteristics.                                                                                                                                                                       | (8M)         |
| 6 | a)       | Explain the AC, DC analysis of JFET.                                                                                                                                                                                                                                            | (8M)         |
|   | b)       | Draw and explain the static and transfer MOSFET characteristics?                                                                                                                                                                                                                | (8M)         |
| 7 | a)       | Draw the circuit diagram of Heartley oscillator, Explain how Barkhausen conditions are satisfied.                                                                                                                                                                               | (8M)         |
|   | b)       | Explain the principle and operation of Negative feed back amplifier? What are the advantages of Negative feed back amplifier?                                                                                                                                                   | (8M)         |

